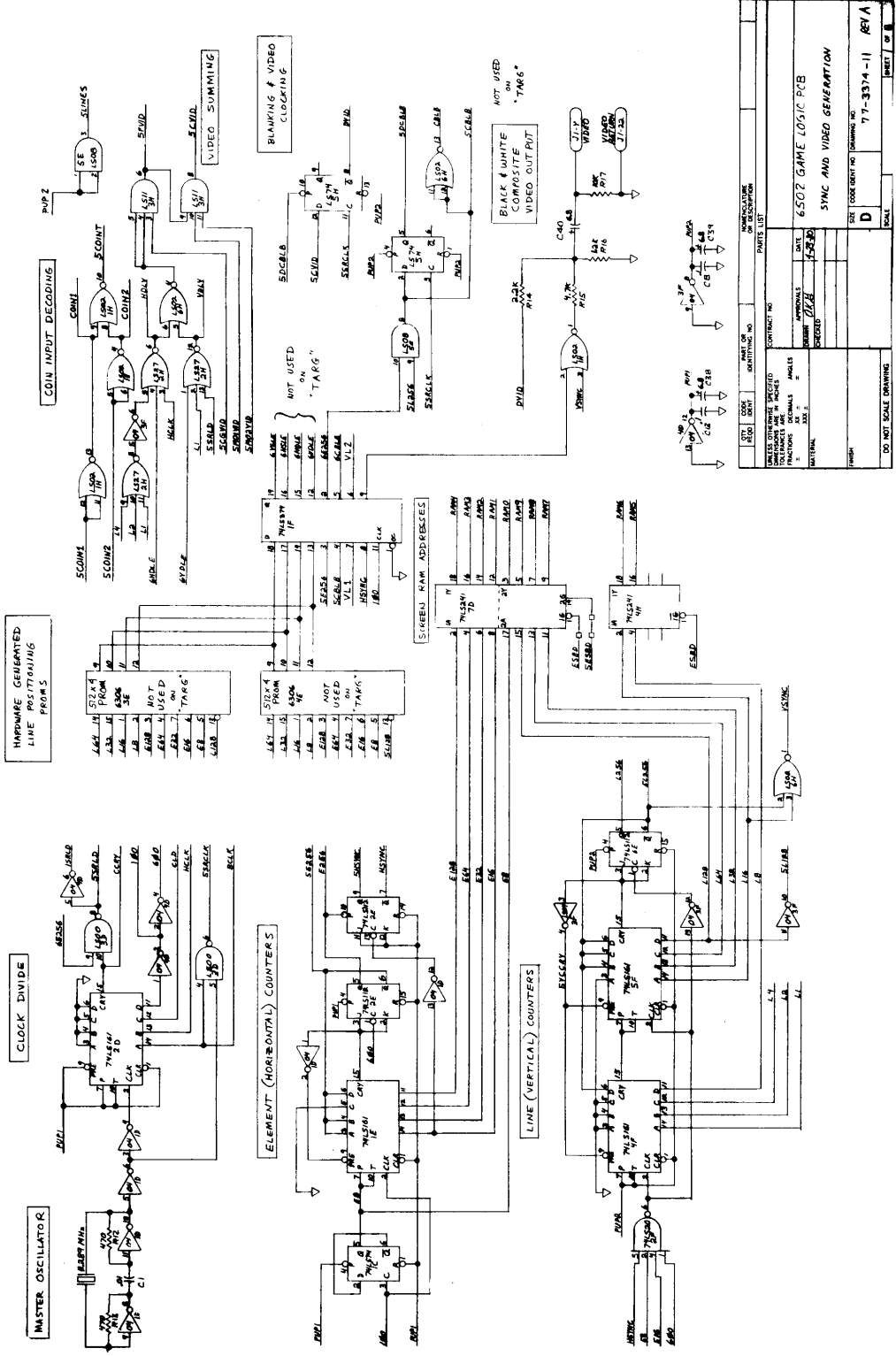


1.0 - 0.5 - 0.25 - 0.125 - 0.0625 - 0.03125



CONTRACT NO.		DATE	
6502 GAME LOGIC PCB		1968	
SYNCH AND VIDEO GENERATION		71-3314-11 REV A	
REVISIONS		SCALE	
1	ORIGINAL	D	1/1
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98	REVISION		
99	REVISION		
100	REVISION		

DO NOT SCALE DRAWING

DATE: 1968

SCALE: 1/1

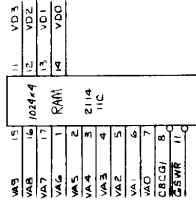
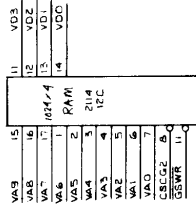
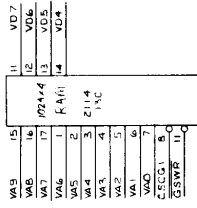
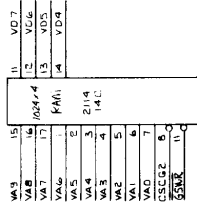
REV: A

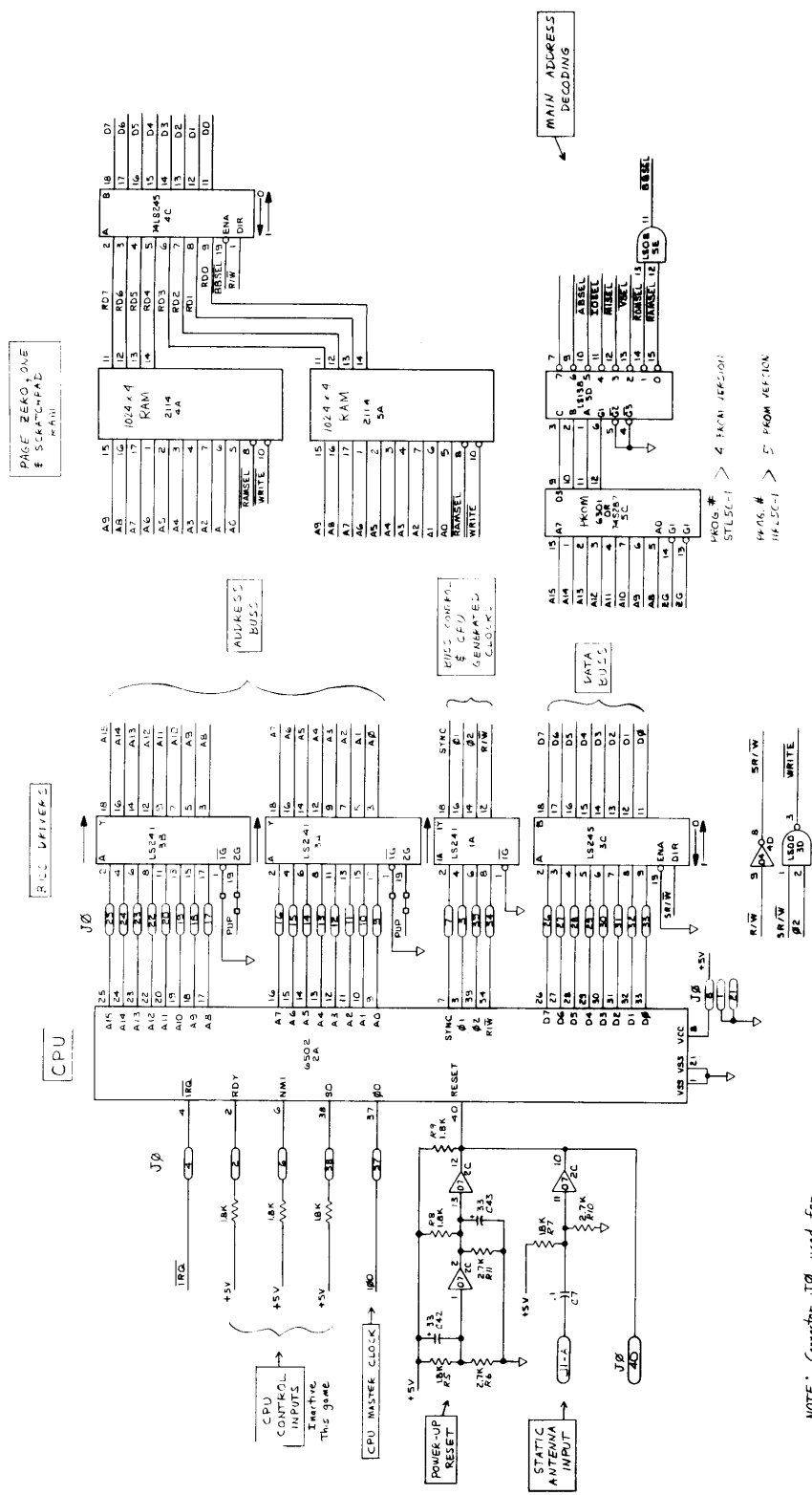
71-3314-11

CHARACTER GENERATOR
IMAGE STORAGE RAMS

For use with PROGRAM
generated image storage master.

SEE "TAGS"

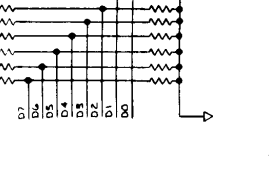
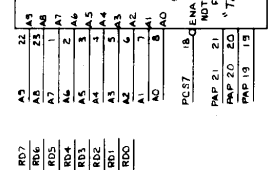
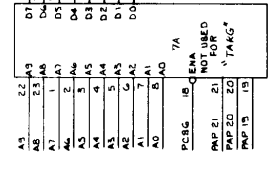
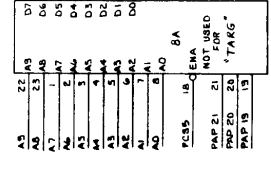
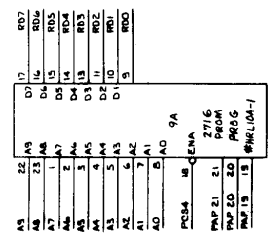
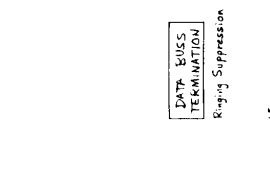
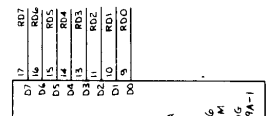
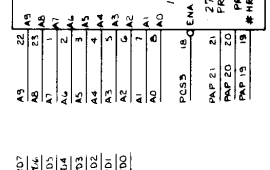
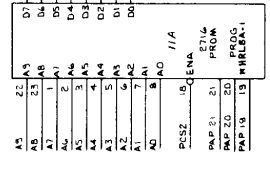
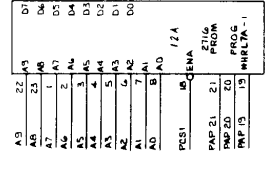
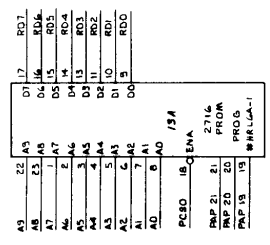




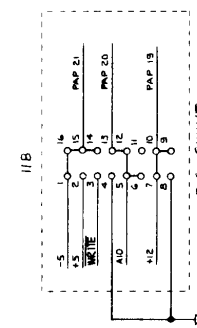
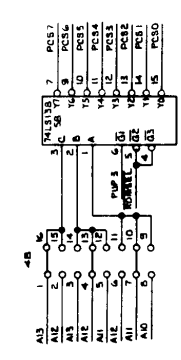
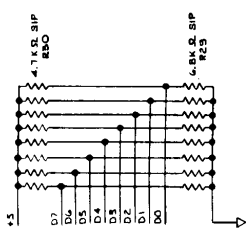
NOTE: Connector J0 used for test & analysis purposes only. Contains all CPU connections.

6502 GAME LOGIC PCB	
DATE	1-23-80
DESIGNED BY	PROGRAM MAN
TESTED BY	ARCHITECTURE
REV	408

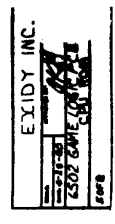
PROGRAM MEMORY

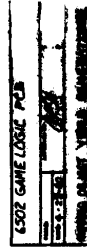
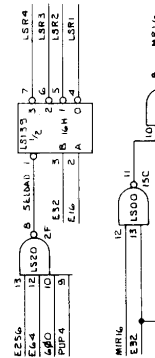
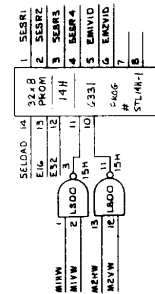
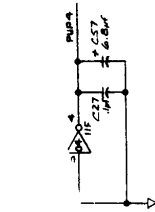
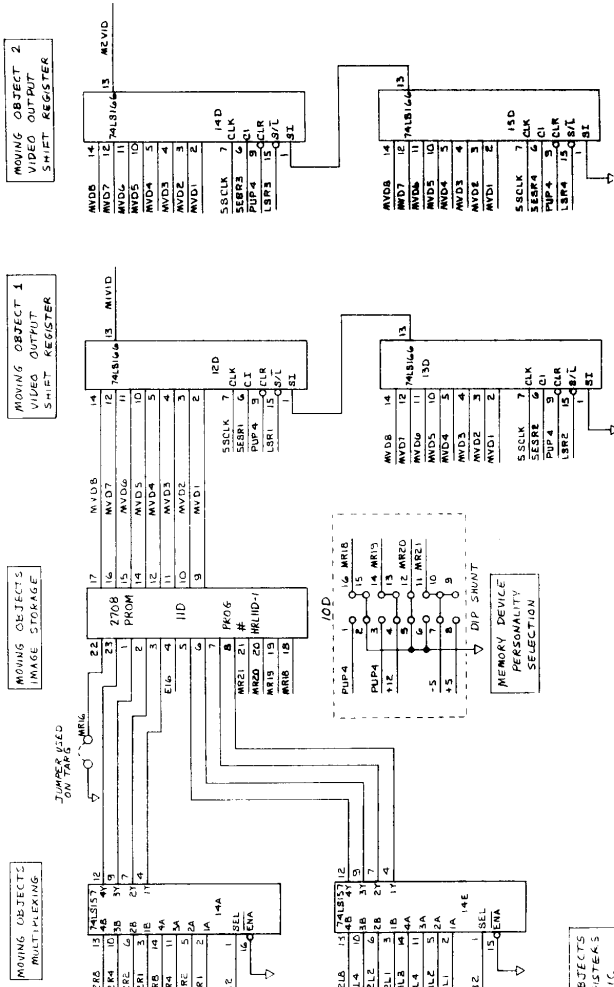


DATA BUS
TERMINATION
Ringing Suppression

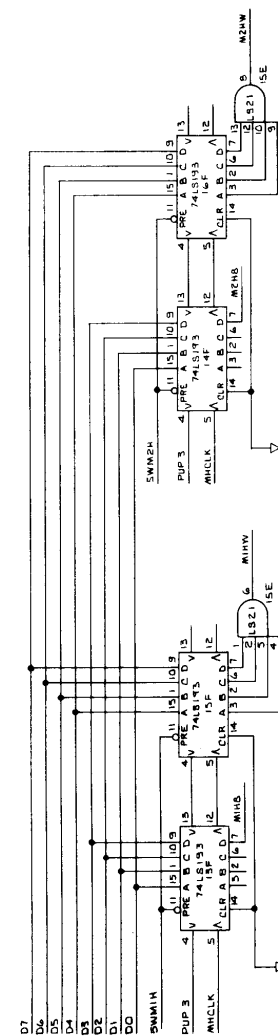


MEMORY DEVICE PERSONALITY
SELECTION
Configuration shown
for "TARG"

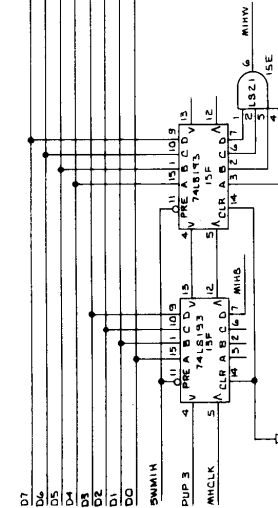




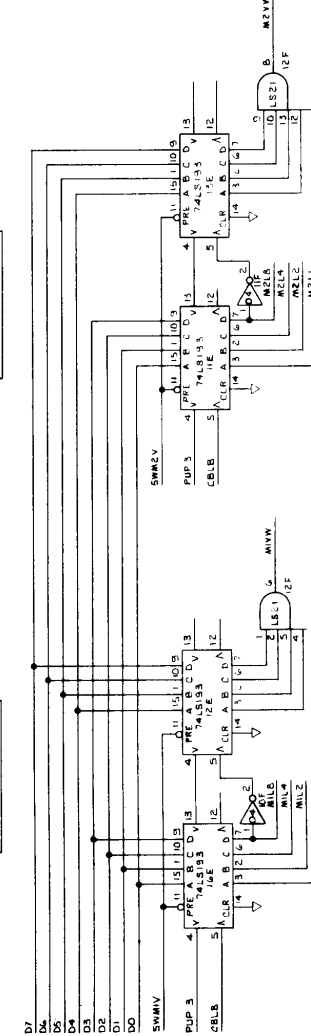
MOVING OBJECT 2
HORZ. COUNTERS



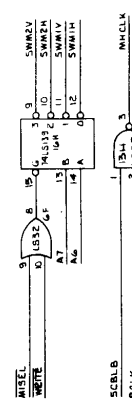
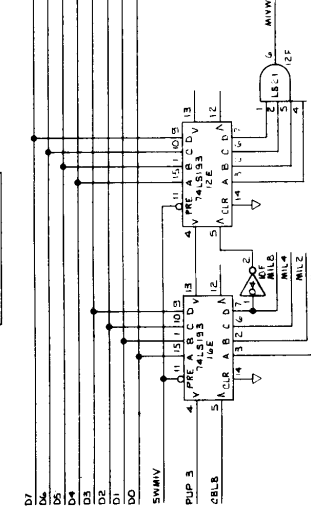
MOVING OBJECT 1
HORZ. COUNTERS



MOVING OBJECT 2
VERTICAL COUNTERS

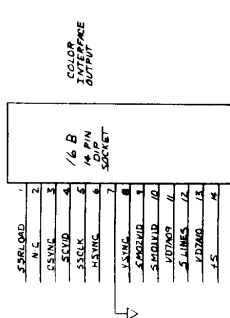


MOVING OBJECT 1
VERTICAL COUNTERS

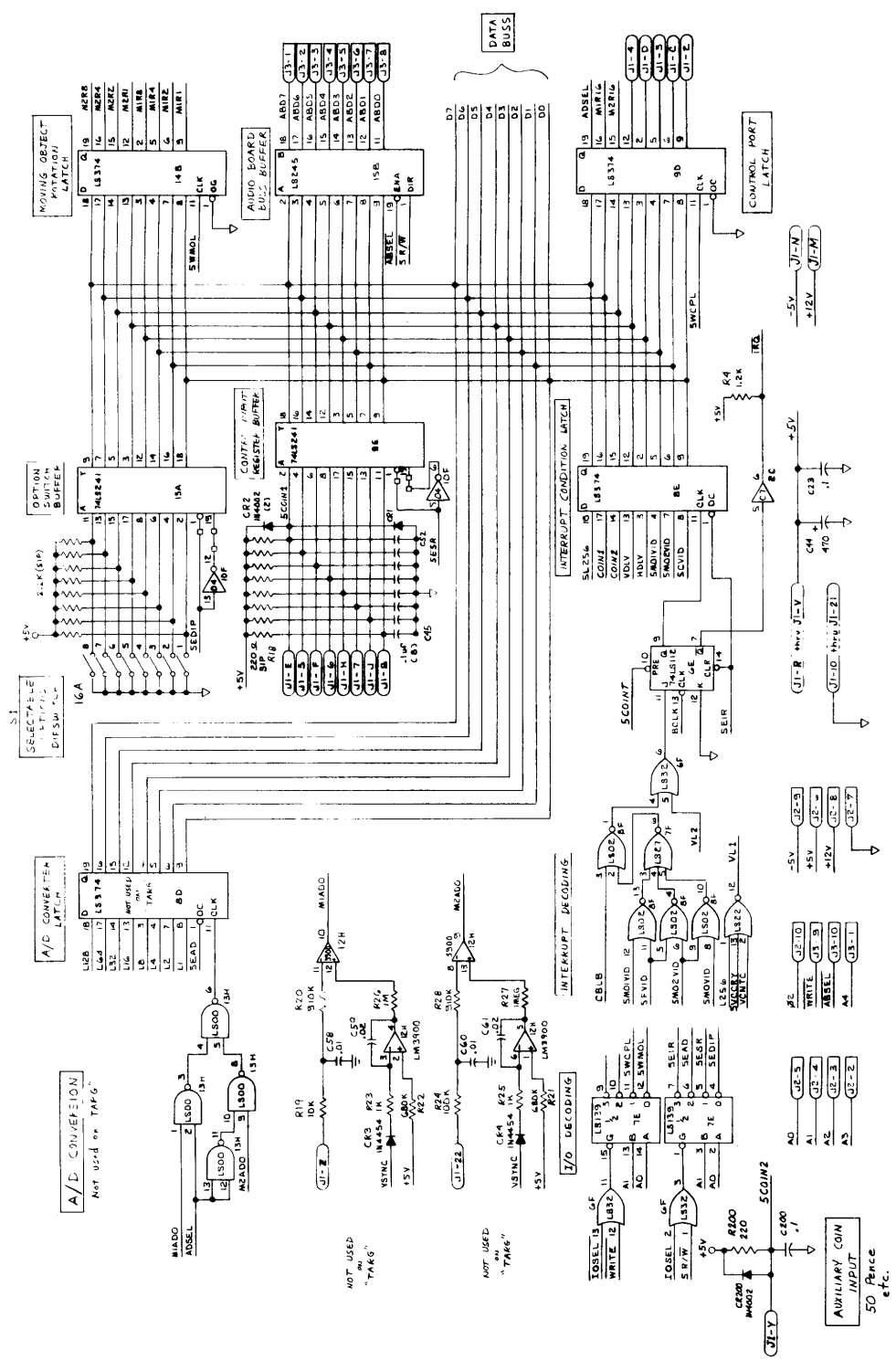


WRITE MOVING OBJECT DECODING

NOTE: FOR "TARG"
MOVING OBJECT 1 = PLAYER IMAGE (NUMBER)
MOVING OBJECT 2 = MISSILE



6702 GAME LOGIC PCB
6702
COLOR OUTPUTS
MOVING OBJECT PARTITION BOARD
8078



74181 ALU
 74182 MUX
 74180 MAJ
 74183 COMB
 74184 COMB
 74185 COMB
 74186 COMB
 74187 COMB
 74188 COMB
 74189 COMB
 74190 COMB
 74191 COMB
 74192 COMB
 74193 COMB
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 74298 COMB
 74299 COMB
 74300 COMB

NOT USED
 "TAGS"
 NOT USED
 "TAGS"
 I/O DECODING
 INTERUPT DECODING
 INTERUPT CONDITION LATCH
 CONTROL PORT LATCH
 MOVING OBJECT SWITCH BUFFER LATCH
 OPTION SWITCH BUFFER
 AUDIO BOARD BUS BUFFER
 CONVERTER REGISTER BUFFER
 A/D CONVERTER LATCH
 SELECTABLE SWITCH DISPLAY
 50 Pence
 etc.

J1-N
 J1-M
 J1-P
 J1-Q
 J1-R
 J1-S
 J1-T
 J1-U
 J1-V
 J1-W
 J1-X
 J1-Y
 J1-Z

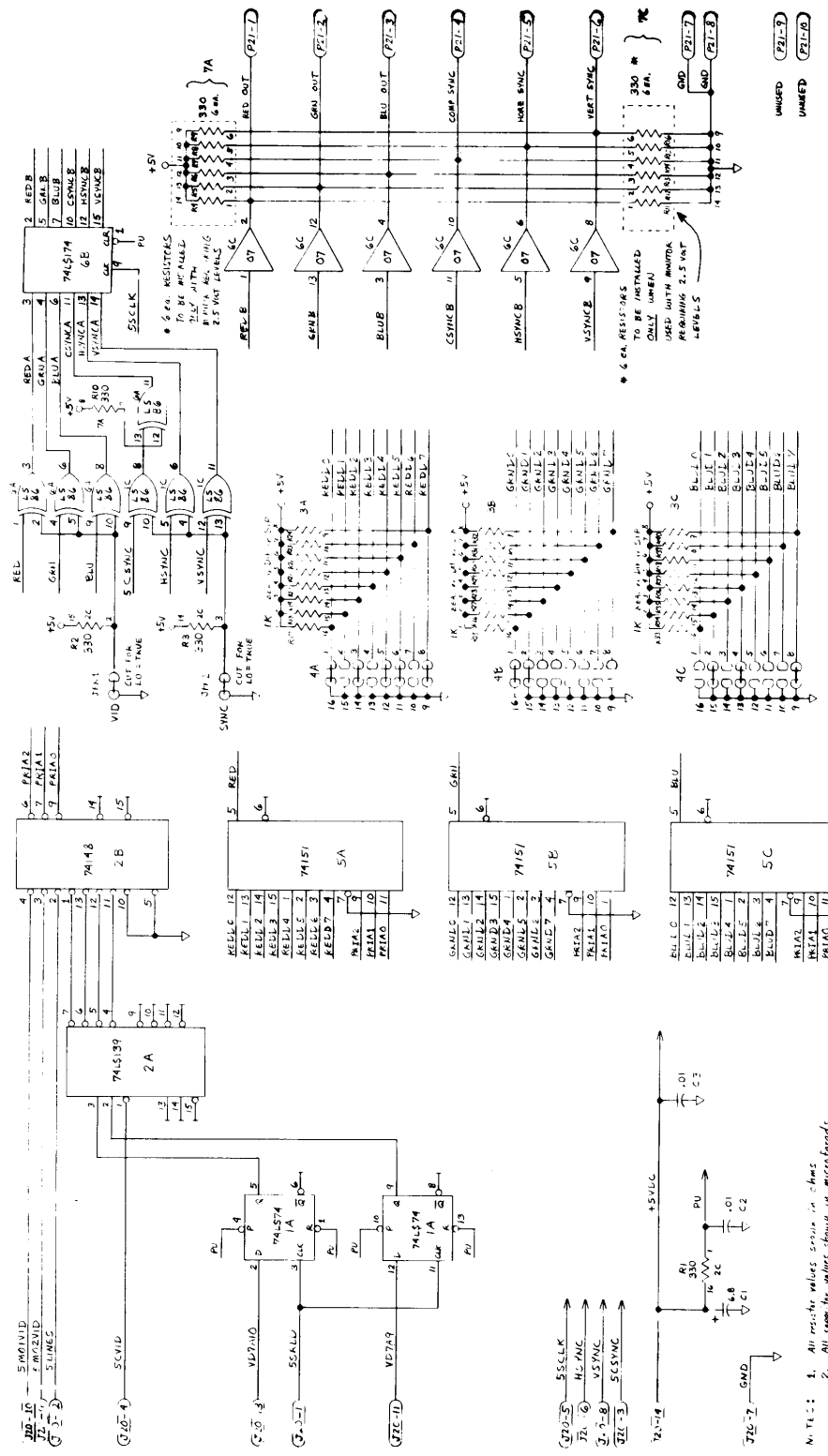
J2-10
 J2-9
 J2-8
 J2-7
 J2-6
 J2-5
 J2-4
 J2-3
 J2-2
 J2-1

J3-10
 J3-9
 J3-8
 J3-7
 J3-6
 J3-5
 J3-4
 J3-3
 J3-2
 J3-1

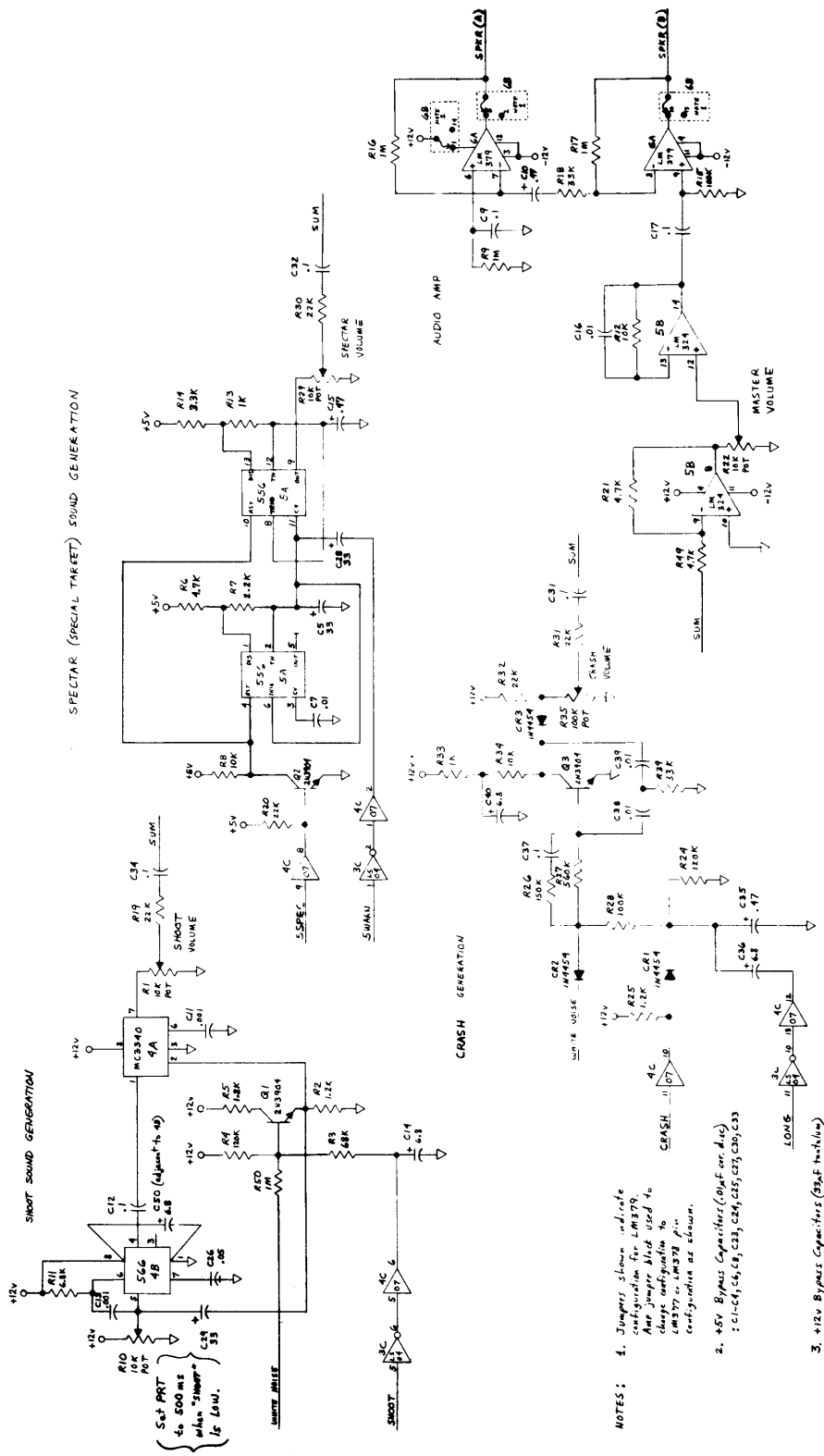
J4-10
 J4-9
 J4-8
 J4-7
 J4-6
 J4-5
 J4-4
 J4-3
 J4-2
 J4-1

J5-10
 J5-9
 J5-8
 J5-7
 J5-6
 J5-5
 J5-4
 J5-3
 J5-2
 J5-1

J6-10
 J6-9
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 J6-6
 J6-5
 J6-4
 J6-3
 J6-2
 J6-1

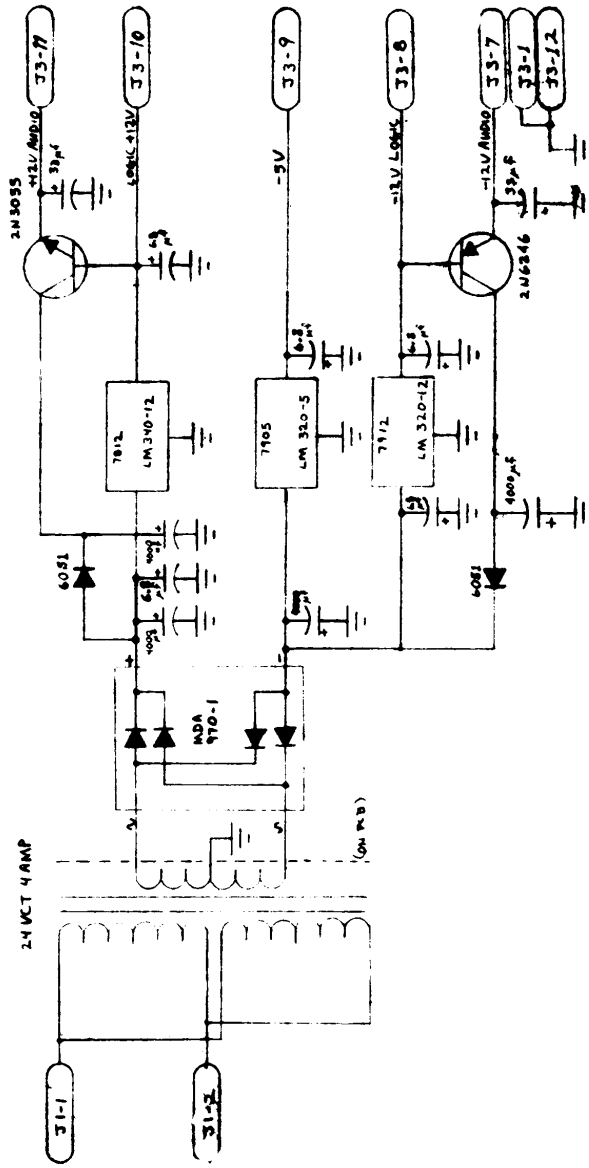


- NOTES:
1. All resistor values shown in Ohms
 2. All capacitor values shown in microfarads
 3. C4-C6 (not shown) are .01 microfarad disc. for bypass



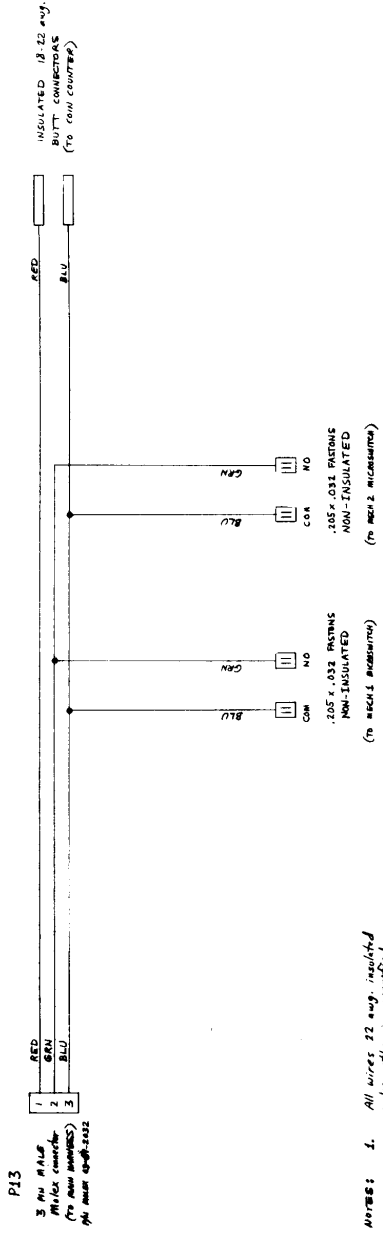
SPECTAR (SPECIAL TARGET) SOUND GENERATION

- NOTES:
1. Jumpers shown indicate configuration for LM379. An jumper block used to change configuration to LM377 or LM378 pin configuration as shown.
 2. +5V Bypass Capacitors (0.01µF or d.c.) : C1-C4, C6, C8, C33, C24, C25, C35, C30, C33
 3. +12V Bypass Capacitors (50µF or 100µF) : C19, C20



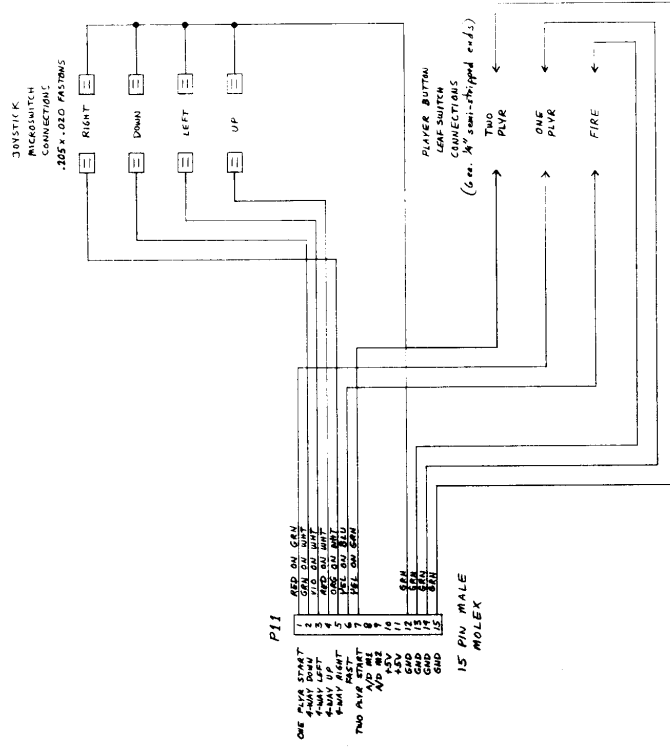
UNIVERSAL POWER SUPPLY SCHEMATIC
 +12.0 V Ambio & Logic, -12.0 V Ambio & Logic
 -5.0 V Logic

TARG
CONV HARNESS SCHEMATIC
(UNIVERSAL CONV HARNESS)



- NOTES: 1. All wires 22 awg. insulated unless otherwise specified.
2. ABSOLUTELY NO SUBSTITUTIONS ON SPECIFIED WIRE COLORS

TARG
CONTROL PANEL HARNESS
SCHEMATIC



- NOTES:
1. All wires 22 awg. Insulated unless otherwise specified.
 2. ABSOLUTELY NO SUBSTITUTIONS ON SPECIFIED WIRE COLORS