

7) PIGGY BACK A 74LS08, CONNECTING PINS 7 & 14 ONLY. 8D

LINK PIN 13 TO +5V

A IS { " " 12 TO PIN 5 OF CPU. A15
" " 11 TO 8E PIN3, 8L PIN 5 AND PIN 4 OF 74LS138 ON EXPANDED BOARD

8) GRAPHICS

PIGGY BACK 2732 ON 2716, LIFTING PINS 20 OF 2716 & 20 OF 2716 AND 20 & 21 OF 2732.

C1T ON C1B

C2T ON C2B

PIGGY BACK A 74LS02 & 74LS04 CONNECTING PINS 7 & 14 ONLY. N3 M3

- CONNECT PIN 1 OF 3K TO PINS 2 & 6 OF O2. AND PINS 21 OF THE 2732's
- CONNECT PIN 6 OF 9N TO PINS 5 & 3 OF O2
- CONNECT PIN 2 OF O4 TO PINS 20 OF THE 2716's
- CONNECT PIN 1 OF O2 TO PINS 20 OF THE 2732's
- CONNECT PIN 4 OF O2 TO PIN 1 OF O4.

u