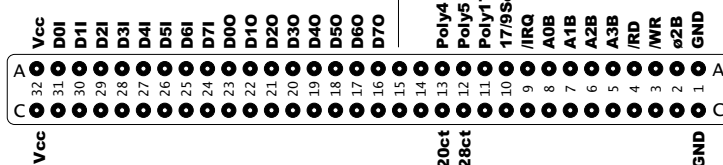


*used to improve signal quality of /CLR

NB: 02B signal for shift registers connected to 750kHz to stop latching problems.

D00 - D70 are fitted with pull up resistors 4k7 not shown on diagram.



As looking at motherboard skt

BREADBOARD POKEY PROJECT

PCB01 - Interface, POT register, timers, and counters.

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